FACULTY OF ENGINEERING

B.E. 2/4 (CSE) I-Semester (Main) Examination, November / December 2012

Subject : Basics Electronics

Time : 3 Hours

Max. Marks: 75

Note: Answer all questions of Part - A and answer any five questions from Part-B.

	PART – A (25 Marks)			
Dis	Distinguish between (Q.No. 1 to 7)			
1.	Intrinsic and extrinsic semiconductors.	(2)		
2.	Diffusion and Drift currents.	(3)		
3.	BJT and FET	(3)		
4.	Positive feedback and negative feedback.	(2)		
5.	Differentiator and integrator.	(2)		
6.	Circuits of HWR and FWR	(3)		
7.	Photodiode and LED	(2)		
8.	Draw the frequency versus gain characteristics of an amplifier with and			
	without negative feedback.	(3)		
9.	Why is SCR known as negative resistance device?	(2)		
10.	What are the effects of negative feedback on amplifiers? (3)			

PART – B (5x10=50 Marks)

 11.(a) Explain what is meant by Draw the VI characteristic diode equation. (b) A 230V, 50Hz voltage is a tapped transformer in a fu Determine : 	forward and reverse biasing a pn-junction diode. s of the diode showing the same with the help of the pplied to the primary of a 5:1 step down, center Il wave circuit having a load of 900 Ω .	(5)
 (i) DC voltage across the I (ii) I_{dc} (iii) DC power delivered to (iv) PIV across each diode 	the load and	(5)
voltage gain, current gain	amplifier connections using BJTs compare and input resistance of these three configurations? s of a zener diode and explain how it can be used	(5) (5)
any of those with a neat d	scillators? Explain the principle of operation of iagram. erties of negative feedback amplifiers.	(5) (5)
(b) What are the different par	Realize a full subtractor using NAND gate only. ameters of an op-amp? Sketch the circuit of a 1 to get V_0 = - (-V ₁ + 2V ₂ - 3V ₃).	(5) (5)
temperature? Explain the	pes of transducers used for the measurement of principle of any of these. CR and VI characteristics, explain its principle of	(5) (5)
with output waveforms.	of a Bridge rectifier and explain its operation ity in a CRO and explain the necessity of a CRO.	(5) (5)
	hree: Iew rate in op-amp Basic logic gates	(10)