

Code No: RR310201

RR

SET-1

**B. Tech III Year I Semester Examinations, December-2011**

**COMPUTER ORGANIZATION**

**(COMMON TO EEE, ECE, EIE)**

**Time: 3 hours**

**Max. Marks: 80**

**Answer any five questions**

**All questions carry equal marks**

---

- 1.a) Explain Von Neumann machine with its architecture.
- b) Discuss unsigned binary multiplication with suitable examples. [16]
- 2.a) Describe various addressing modes.
- b) Explain register organization. [16]
- 3.a) What is pipelining? Explain instruction pipelining.
- b) Give a note on RISC architecture. [16]
- 4.a) List the applications of microprogramming.
- b) What is an assembly language? Write a program to print first 'n' number. [16]
- 5.a) Compare and contrast hardwired control with micro program control.
- b) Describe Dynamic RAM organization. [16]
- 6.a) List various types of semiconductor memory.
- b) Explain virtual memory address translation. [16]
- 7.a) What is segmentation? Differentiate it from paging.
- b) Discuss bus arbitration techniques. [16]
- 8.a) Describe interrupt driven I/O.
- b) Explain direct memory access. [16]

--ooOoo--

Code No: RR310201

RR

SET-2

**B. Tech III Year I Semester Examinations, December-2011**

**COMPUTER ORGANIZATION**

**(COMMON TO EEE, ECE, EIE)**

**Time: 3 hours**

**Max. Marks: 80**

**Answer any five questions  
All questions carry equal marks**

---

- 1.a) What is pipelining? Explain instruction pipelining.
- b) Give a note on RISC architecture. [16]
- 2.a) List the applications of microprogramming.
- b) What is an assembly language? Write a program to print first 'n' number. [16]
- 3.a) Compare and contrast hardwired control with micro program control.
- b) Describe Dynamic RAM organization. [16]
- 4.a) List various types of semiconductor memory.
- b) Explain virtual memory address translation. [16]
- 5.a) What is segmentation? Differentiate it from paging.
- b) Discuss bus arbitration techniques. [16]
- 6.a) Describe interrupt driven I/O.
- b) Explain direct memory access. [16]
- 7.a) Explain Von Neumann machine with its architecture.
- b) Discuss unsigned binary multiplication with suitable examples. [16]
- 8.a) Describe various addressing modes.
- b) Explain register organization. [16]

--ooOoo--

Code No: RR310201

RR

SET-3

**B. Tech III Year I Semester Examinations, December-2011**

**COMPUTER ORGANIZATION**

**(COMMON TO EEE, ECE, EIE)**

**Time: 3 hours**

**Max. Marks: 80**

**Answer any five questions**

**All questions carry equal marks**

---

- 1.a) Compare and contrast hardwired control with micro program control.
- b) Describe Dynamic RAM organization. [16]
- 2.a) List various types of semiconductor memory.
- b) Explain virtual memory address translation. [16]
- 3.a) What is segmentation? Differentiate it from paging.
- b) Discuss bus arbitration techniques. [16]
- 4.a) Describe interrupt driven I/O.
- b) Explain direct memory access. [16]
- 5.a) Explain Von Neumann machine with its architecture.
- b) Discuss unsigned binary multiplication with suitable examples. [16]
- 6.a) Describe various addressing modes.
- b) Explain register organization. [16]
- 7.a) What is pipelining? Explain instruction pipelining.
- b) Give a note on RISC architecture. [16]
- 8.a) List the applications of microprogramming.
- b) What is an assembly language? Write a program to print first 'n' number. [16]

--ooOoo--

Code No: RR310201

RR

SET-4

**B. Tech III Year I Semester Examinations, December-2011**  
**COMPUTER ORGANIZATION**  
**(COMMON TO EEE, ECE, EIE)**

**Time: 3 hours**

**Max. Marks: 80**

**Answer any five questions**  
**All questions carry equal marks**

---

- 1.a) What is segmentation? Differentiate it from paging.
- b) Discuss bus arbitration techniques. [16]
- 2.a) Describe interrupt driven I/O.
- b) Explain direct memory access. [16]
- 3.a) Explain Von Neumann machine with its architecture.
- b) Discuss unsigned binary multiplication with suitable examples. [16]
- 4.a) Describe various addressing modes.
- b) Explain register organization. [16]
- 5.a) What is pipelining? Explain instruction pipelining.
- b) Give a note on RISC architecture. [16]
- 6.a) List the applications of microprogramming.
- b) What is an assembly language? Write a program to print first 'n' number. [16]
- 7.a) Compare and contrast hardwired control with micro program control.
- b) Describe Dynamic RAM organization. [16]
- 8.a) List various types of semiconductor memory.
- b) Explain virtual memory address translation. [16]

--ooOoo--