





## **DEPARTMENT OF ELECTRONICS AND COMMUNICATION** ENGINEERING **QUESTION BANK**

#### SUB.NAME : LINEAR INTEGRATED CIRCUITS BRANCH : ECE YEAR / SEMESTER : II / IV

SUB CODE : EC2252

## UNIT- I

### IC FABRICATION AND CIRCUIT CONFIGURATION FOR LINEAR IC's Part A (2 Marks)

- 1. What are the popular IC packages available?
- 2. Define CMRR of an op-amp.
- 3. What causes slew rate?
- 4. Define PSRR or SVRR or PSS.
- 5. Define slew rate.
- 6. What is the need for frequency compensation in practical op-amps?
- 7. What do you mean by balanced and unbalanced output?
- 8. What is loading effect? How can you reduce it?
- 9. What is an ideal op-amp?
- 10. What is input offset voltage?
- 11. What is input off set current?
- 12. What is input bias current?
- 13. Draw the circuit of a Widlar current source and write the expression for its output current?

## Part B (16 Marks)

1. Obtain the frequency response of an open-loop op-amp	(16)	
2. Explain the operation of a basic differential amplifier?	(16)	
3. Derive the transfer characteristics of differential amplifier or sketch the transfer characteristics		
of dual input differential amplifier showing the linear and limiting regions. Comment on the		
same.	(16)	
4. Design an active load for an emitter-coupled pair (differential amplifier) and perform a detailed		
analysis to find its differential mode gain and the output resistance.	(16)	
5. Design a Widlar current source and obtain the expression for output current. Also prove that		
widlar current source has better sensitivity than constant current source.	(16)	
6. Explain the supply independent biasing technique using VBE as the reference voltage. Also, find		
the dependence of its output current on temperature.	(16)	
7. Explain supply independent biasing using Zener-referenced bias circuit. Also, design a		
temperature compensated Zener-reference source.	(16)	

- temperature compensated Zener-reference source.
- 8. Discuss about the methods of frequency compensation. (16)

### UNIT II APPLICATIONS OF OPERATIONAL AMPLIFIERS Part A (2 Marks)

1. Mention some of the linear applications of op – amps:

2. Mention some of the non - linear applications of op-amps:-

3. What are the areas of application of non-linear op- amp circuits?

4. What is the need for an instrumentation amplifier?

5. List the features of instrumentation amplifier:

6. What are the applications of V-I converter?

7. What do you mean by a precision diode?

8. Write down the applications of precision diode.

9. List the applications of Log amplifiers:

10. What are the limitations of the basic differentiator circuit?

11. Write down the condition for good differentiation :-

12. What is a comparator?

13. What are the applications of comparator?

14. What is a Schmitt trigger?

15. What is a multivibrator?

16. What do you mean by monostable multivibrator?

17. What is an astable multivibrator?

18. What is a bistable multivibrator?

19. What are the requirements for producing sustained oscillations in feedback circuits? For sustained oscillations,

20. Mention any two audio frequency oscillators:

21. What are the characteristics of a comparator?

22. What is a filter?

23. What are the demerits of passive filters?

24. What are the advantages of active filters?

25. Mention some commonly used active filters:

### Part B (16 Marks)

(16)

1. Discuss the need for an instrumentation amplifier? Give a detailed analysis for the same. (16)

2. Explain the operation of the Schmitt trigger.

3. Detail the logarithmic and anti logarithmic amplifier? (16)

4. With circuit diagram discuss the following applications of opamp. (16)

5. Draw the circuit diagram of a second order Butterworth active low pass filter and derive an expression for its transfer function and explain a second order low pass filter (16)

#### UNIT III

# ANALOG MULTIPLIER AND PLL

## Part-A ( 2Marks )

1. Mention some areas where PLL is widely used:

2. List the basic building blocks of PLL:

3. What are the three stages through which PLL operates?

4. Define lock-in range of a PLL:

5. Define capture range of PLL:

6. Define Pull-in time.

7. For perfect lock, what should be the phase relation between the incoming signal and VCO output signal?

8. Give the classification of phase detector:

9. What is a principle of analog phase detector?

10. What are the problems associated with switch type phase detector?

11. What is a voltage controlled oscillator?

12. On what parameters does the free running frequency of VCO depend on?

13. Give the expression for the VCO free running frequency.

14. Define Voltage to Frequency conversion factor.

15. What is the purpose of having a low pass filter in PLL?

16. Discuss the effect of having large capture range.

17. Mention some typical applications of PLL:

18. What is a compander IC? Give some examples.

19. What are the merits of companding?

20. List the applications of OTA:

### Part B (16 Marks)

1. Briefly explain the block diagram of PLL and derive the expression for Lock range and capture range. (16)

2. With a neat functional diagram, explain the operation of VCO. Also derive an expression for fo.

- (16)
- 3. Analyze the Gilbert's four quadrant multiplier cell with a neat circuit diagram. Discuss its applications. (16)
- 4. In detail discuss the applications of PLL: (16)

5. With a neat sketch, explain the working of variable transconductance multiplier. (16)

## UNIT-IV

#### ANALOG TO DIGITAL AND DIGITAL TO ANALOG CONVERTERS Part A (2 Marks)

1. List the broad classification of ADCs.

2. List out the direct type ADCs.

3. List out some integrating type converters.

4. What is integrating type converter?

5. Explain in brief the principle of operation of successive Approximation ADC.

6. What are the main advantages of integrating type ADCs?

7. Where are the successive approximation type ADC's used?

8. What is the main drawback of a dual-slop ADC?

9. State the advantages of dual slope ADC:

10. Define conversion time.

11. Define resolution of a data converter.

12. Define accuracy of converter.

13. What is settling time?

14. Explain in brief stability of a converter:

15. What is meant by linearity?

16. What is monotonic DAC?

17. What is multiplying DAC?

18. What is a sample and hold circuit? Where it is used?

19. Define sample period and hold period.

20. What is meant by delta modulation?

## Part B (16 Marks)

1. What is integrating type converter? Explain the operation of dual slope ADC:		(16)
2. Explain the principle of operation of successive Approximation ADC.		(16)
3 .Explain the operation of sample and hold circuit.	(16)	
4. Explain the various types of digital to analog converters:		(16)

5. What is delta sigma modulation? Explain the A/D conversion using Delta modulator. (16)

#### **UNIT-V**

### WAVEFORM GENERATORS AND SPECIAL FUNCTION ICs Part A (2Marks)

1. Mention some applications of 555 timer:

2. List the applications of 555 timer in monostable mode of operation:

3. List the applications of 555 timer in Astable mode of operation:

4. What is a voltage regulator?

5. Give the classification of voltage regulators:

6. What is a linear voltage regulator?

7. What is a switching regulator?

8. What are the advantages of IC voltage regulators?

9. Give some examples of monolithic IC voltage regulators:

10. What is the purpose of having input and output capacitors in three terminal IC regulators?

11. Define line regulation.

12. Define load regulation.

13. What is meant by current limiting?

14. Give the drawbacks of linear regulators:

15. What is the advantage of switching regulators?

16. What is an opto-coupler IC? Give examples.

17. Mention the advantages of opto-couplers:

18. What is an isolation amplifier?

19. What is the need for a tuned amplifier?

20. Give the classification of tuned amplifier:

### Part B (16 Marks)

1. What is 555 timers? What are the features of 555 timers? Explain the monostable mode in detail? (16)

2. Explain the Astable mode of operation using 555 timer. (16)3. In detail discuss the 723 IC general purpose voltage regulator. (16)

4. Explain the operation of switching regulators. Give its advantages.

(16)5. Explain the functional diagram of LM 380 power amplifier. (16)

6. Discuss in detail the operation of Astable multivibrator.

(16)(16)7. Discuss in detail the operation of Monostable multivibrator.

8. What are the requirements for producing sustained oscillations in feedback circuits? Discuss any two audio frequency oscillators. (16)

9. Write a note on

1. Isolation amplifier.

2. Video amplifier.

3. Audio power amplifier.

(16)