Roll No.								Ser Q.			
ఒటు	ಪ್ರಶಿಕ	ಕಳ ಸ	റെജ്	. 9 ]					[:	. ه. د	٦-

ಸಂಕೇತ ಸಂಖ್ಯೆ: 73 ವಿಷಯ: ಎಲಿಮೆಂಟ್ಸ್ ಆಫ್ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಇಂಜಿನಿಯರಿಂಗ್

Code No.: 73 Subject: ELEMENTS OF ELECTRONICS ENGINEERING

ದಿನಾಂಕ : 06. 04. 2013] [ Date : 06. 04. 2013

ಸಮಯ : ಬೆಳಿಗ್ಗೆ 9-30 ರಿಂದ ಮಧ್ಯಾಹ–12-45 ರವರೆಗೆ ] [ Time : 9-30 A.M. to 12-45 P.M.

ಪರಮಾವಧಿ ಅಂಕಗಳು : 90 ] [ Max. Marks : 90

## FOR OFFICE USE ONLY

FOR OFFICE USE ONE!									
Q. No.	Marks	Q. No.	Marks	Q. No.	Marks	Q. No.	Marks	Q. No.	Marks
1.		×		×		×		×	
2.		×		×		×		×	
3.		×		×		×		×	
4.		×		×		×		×	
5.		×		×		×		×	
6.		×		×		×		×	
7.		×		×		×		×	
8.		×		×		×		×	
9.		×		×		×		×	
×		×		×		×		×	
×		×		×		×		×	
×		×		×		×		×	
×		×		×		×		×	
					<u> </u>	To	otal M	arks	
Total Marks in words							Gra	nd Total	
1. ✓									
2. ✔					✓		✓		
Signature of Evaluators		uators	Registra	tion No.	Sign De	Signature of the Signature of the Deputy Chief Invigilate			the Room ator

## General Instructions:

- i) The Question-cum-Answer Booklet consists of 9 objective and subjective types of questions.
- ii) Space has been provided against each objective type question. You have to choose the correct choice and write the complete answer in the space provided.
- iii) For subjective type questions enough space for each question has been provided. You have to answer the questions in the space.
- iv) Follow the instructions given against both the objective and subjective types of questions.
- v) Candidate should not write the answer with pencil. Answers written in pencil will not be evaluated (Except Graphs, Diagrams & Maps).
- vi) In case of Multiple Choice, Fill in the blanks and Matching questions, scratching / rewriting / marking is not permitted, thereby rendering to disqualification for evaluation.
- vii) For reading the questions 15 minutes of extra time has been provided.

*Note :* Answer *all* the questions.

1.	Fill	in the blanks with the appropriate figure/word(s) by selecting from the choices
	give	en in the brackets: $10 \times 1 = 10$
	i)	The middle layer of <i>P-N</i> junction transistor is
		( lightly doped, heavily doped, normally doped )
		Ans :
	ii)	Germanium is a
		Ans:
	iii)	A very sensitive diode is
		( P-N junction diode, silicon diode, germanium diode )
		Ans :

iv)	VLSI circuit has
	( 400 gates, more than 400 gates, less than 400 gates)
	Ans :
v)	Linear I.C. is also known as
	( digital I.C., monolithic I.C. , hybrid I.C. )
	Ans :
vi)	A microprocessor consists of
	( SSI & MSI, LSI & MSI, LSI & VLSI )
	Ans:
vii)	The cost of an Op-Amp is
	( high, low, very high )
	Ans :
viii)	Intel 8085 has a word length of
	(8 bits, 4 bits, 16 bits)
	Ans :

	1X)	A logical block is used for storage and tra system is called	_	niormation in a digital
			( shift register	, buffer register, register)
		Ans:		
	x)	The binary system consists is	of two	digits only, that
2.	a)	What is LED ? Mention any two application	ns of LED.	4
				_

b)	Define the term 'rectification'. Draw a neat circuit diagram of half-wave rectified
	and label the parts.

	c)	Name the two types of transistors.	2
3.	a)	What do you mean by <i>P</i> -type material ?	2
	b)	Define <i>P-N</i> junction diode and draw a neat symbol.	4

	c)	List any four differences between germanium and silicon diodes. 4
4.	a)	Define an operational amplifier. 2
	b)	Explain the functions of input stage (differential amplifier) and output stage of an
		Op-Amp.

c)	Mention any four characteristics of Op-Amp.	
	-	
a)	What do you mean by I.C. ?	

6.

b) Convert  $815_8$  into decimal number.

c) Convert 1512 into binary number.

7.	a)	Name two types of I.C. packages.	2

Write a neat symbol of I.C.

b)

11

**73** 

c) Draw the neat sketches of the following I.C. packages:

- i) T 0-5
- ii) DIL.

9.	a)	What is a microprocessor?	2
	b)	List any three applications of microprocessor.	3
			_

- c) Write the symbols and truth tables of the following gates:
  - i) NOT
  - ii) AND.